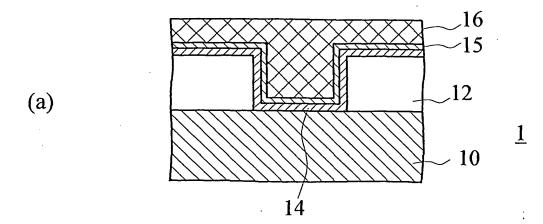
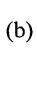
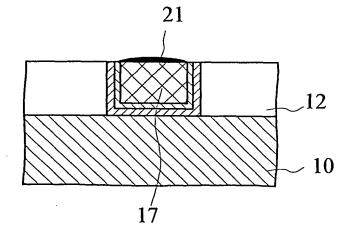
Fig. 1

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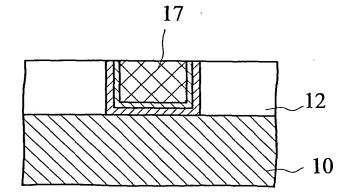
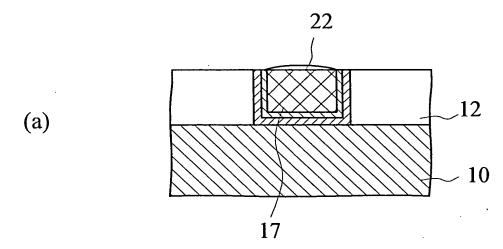


Fig. 2



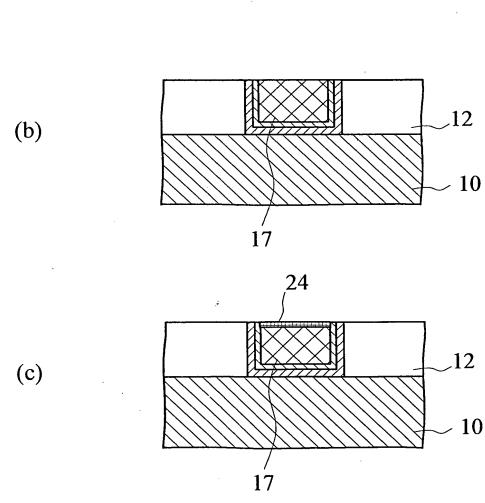
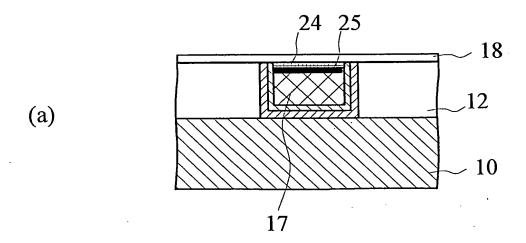
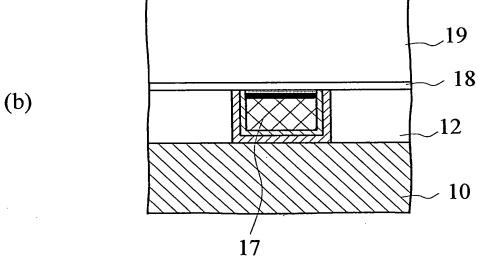


Fig. 3





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Fig. 5

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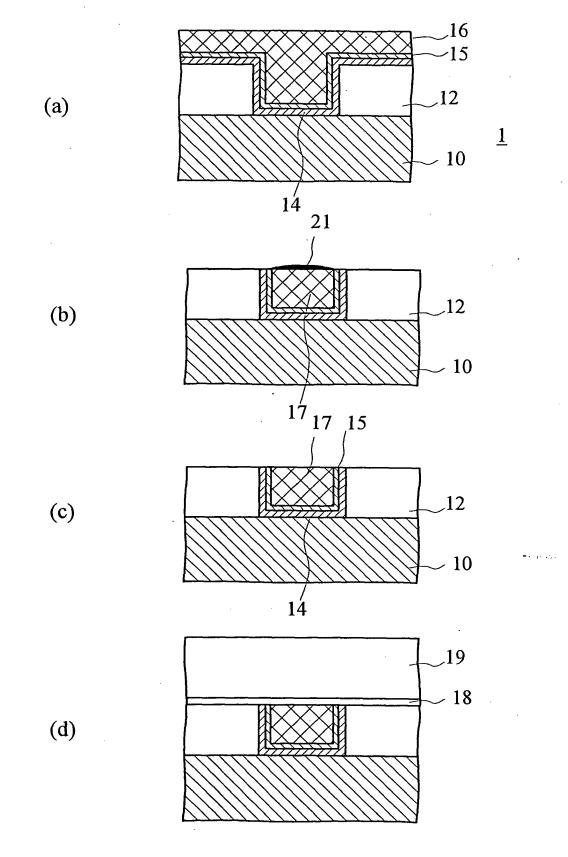
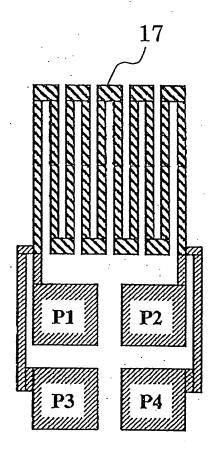
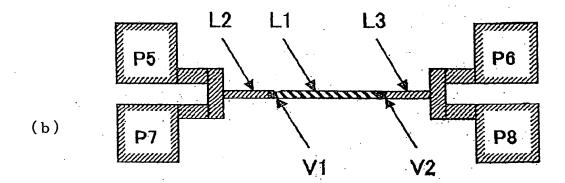


Fig. 6

(a)



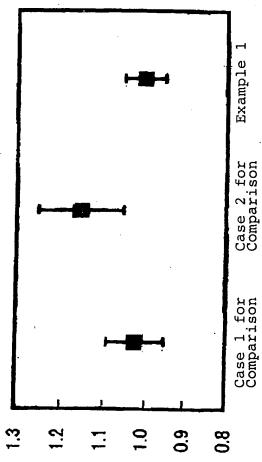


L 1 : Measured Section of the Interconnection

 $L\ 2\,,\ L\ 3$  : Interconnections lying at the Upper Level Than L1

 $V\ 1$  : Via Plug to Connect L1 with L2

 $V\ 2$  : Via Plug to Connect L1 with L3



Interconnection Resistance

Fig.

